

11000 U.S. PTO  
09/854269



Class	Subclass	ISSUE CLASSIFICATION

**P**

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PATENT NUMBER

**U.S. UTILITY Patent Application**

10 SCANNED	O.I.P.E.	PATENT DATE
Q.A.		

APPLICATION NO. 09/854269	CONT/PRIOR D	CLASS 257	SUBCLASS 77-8	ART UNIT 2822	EXAMINER Lewis
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APPLICANTS

Thomas DiStefano

TITLE

Microelectronic packages with solder interconnections

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE FEE</b>	
				Amount Due	Date Paid
				<b>ISSUE BATCH NUMBER</b>	

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